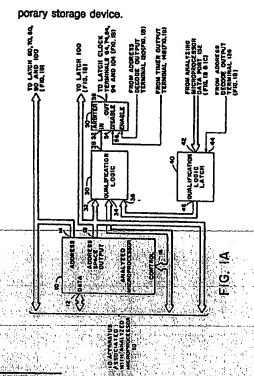
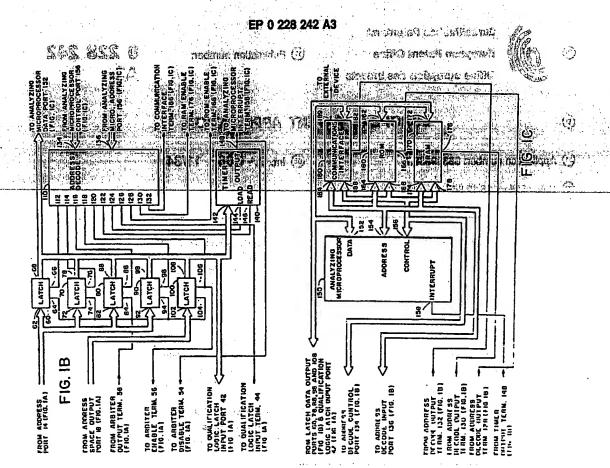
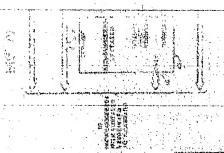


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- Non-intrusive microprocessor performance analyzer.
- A set of addresses contained within an address space of an analyzed processor are selected for monitoring according to qualification criteria. Thereafter, address information is placed in a data structure within a control device. Control information from the analyzed processor is monitored and compared against the qualification criteria, and responsive to control information from the analyzed processor agreeing with the qualification criteria, information concurrently present on an address port of the analyzed processor is placed in a temporary storage device. Placement of information in the temporary storage device operates to replace information previously stored therein. A timing device functions to produce indications at regular intervals of time, asychronous with the operation of the analyzed processor. The control device functions, responsive to the coindications produced by the timing device, to read the information present in the temporary storage device, compare said information against the set of addresses, and count the number of times the in-formation read from the temporary storage device agrees with information contained within the set of addresses. A count is also maintained of the total number of times information is read from the tem-



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	Category	Citation of document with indication, where appropriate, 10/relevant passages // US=A=4 382-17/9 (NGR CORP*) **Abstract; claims 1, 2, 6, 7, 15-20 **	Relevant to claim	CLASSIFICATION OF THE APPLICATION (dr. Cl.4)	The state of the s
- 4	A	US-A-4 511 961 (NCR CORP.) * Abstract; claims 1-3,5-7 *	1,2,6	A "1996年1984年1986年1986年1986年1986年1986年1986年1986年1986	
-	A	ELECTRONIQUE INDUSTRIELLE, no. 61, November 1983, pages 102-109, Paris, FR; G. LAGRANGE: "Les mathématiques au secours de l'analyse logique" * Page 105, column 3, line 40 - page 106, column 1, line 28 *	1,2,6		
				TECUDICAL PROPERTY	
				TECHNICAL PIELDS SEARCHED (Int. CL4) G 06 F 11/34	
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